H6/9-28-01
N. Jones

Patent
Attorney's Docket No. 040060-113

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Lars BOHLIN

Application No.: 09/590,172

Filed: June 9, 2000

For: A METHOD OF SUPERVISING PARALLEL PROCESSES

Group Art Unit: 2184

Examiner: Unassigned

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

[X] No additional fee for submission of an IDS is required.

[ ] The fee of $180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.

[ ] A certification under 37 C.F.R. § 1.97(e) is also enclosed.

[ ] A certification under 37 C.F.R. § 1.97(e), and the fee of $180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.

[ ] Charge $_____ to Deposit Account No. 02-4800 for the fee due.

[ ] A check in the amount of $_____ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: _____
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Date: April 3, 2001

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	)	

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

U.S. Patent No. 4,371,754

U.S. Patent No. 4,700,292

U.S. Patent No. 4,819,232

EP 0 752 656 A2

W.W. Wesley Peterson and E.J. Weldon, Jr. "Error Correcting Codes", MIT Press, Cambridge, 1972"

C.L. Chen and M.Y. Hsiao "Error-Correcting Codes for Semiconductor memory Applications: A State-of-the-Art Review, IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 124-134.

J. Arlat and W.C. Carter "Implementation and Evaluation of a (b,k) - Adjacent Error-Correcting/Detecting Scheme for Supercomputer Systems", IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 159-169

Copy of International-Type Search Report

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: _____

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Alexandria, Virginia 22313-1404
(703) 836-6620

Date: April 3, 2001

INFORMATION DISCLOSURE CITATION

PTO-1449

ATTORNEY'S DKT NO.	APPLICATION NO.
040060-113	09/590,172
APPLICANT: Lars BOHLIN	
FILING DATE: June 9, 2000	GROUP: 2184

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,371,754	02/01/83	De et al.			
	4,700,292	10/13/87	Campanini			
	4,819,232	04/04/89	Krings			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes	Translation No
	0752656A2	01/08/97	EP				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	W.W. Wesley Peterson and E.J. Weldon, Jr. "Error Correcting Codes", MIT Press, Cambridge, 1972"
	C.L. Chen and M.Y. Hsiao "Error-Correcting Codes for Semiconductor memory Applications: A State-of-the-Art Review, IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 124-134.
	J. Arlat and W.C. Carter "Implementation and Evaluation of a (b,k) - Adjacent Error-Correcting/Detecting Scheme for Supercomputer Systems", IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 159-169

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.